



PALM INTRANET

Day : Monday
Date: 2/3/2003
Time: 15:28:37

Inventor Name Search Result

Your Search was:

Last Name = AGARWAL
First Name = VISHNU K.

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09454826	6245631	150	12/06/1999	METHOD OF FORMING BURIED BIT LINE MEMORY CIRCUITRY AND SEMICONDUCTOR PROCESSING METHOD OF FORMING A CONDUCTIVE LINE	AGARWAL , VISHNU K.
09200253	6303972	150	11/25/1998	DEVICE INCLUDING A CONDUCTIVE LAYER PROTECTED AGAINST OXIDATION	AGARWAL , VISHNU K.
09201576	6206759	150	11/30/1998	POLISHING PADS AND PLANARIZING MACHINES FOR MECHANICAL OR CHEMICAL-MECHANICAL PLANARIZATION OF MICROELECTRONIC-DEVICE SUBSTRATE ASSEMBLIES, AND METHODS FOR MAKING AND USING SUCH PADS AND MACHINES	AGARWAL , VISHNU K.
09250974	6342417	150	02/16/1999	METHODS OF FORMING MATERIALS COMPRISING TUNGSTEN AND NITROGEN	AGARWAL , VISHNU K.
09251104	6387748	150	02/16/1999	METHODS OF FORMING SEMICONDUCTOR CIRCUIT CONSTRUCTIONS AND CAPACITOR CONSTRUCTIONS	AGARWAL , VISHNU K.
09291127	6218256	150	04/13/1999	METHODS OF MANUFACTURING ELECTRODE AND CAPACITOR STRUCTURES FOR A SEMICONDUCTOR DEVICE	AGARWAL , VISHNU K.
09310408	6297527	150	05/12/1999	MULTILAYER ELECTRODE FOR FERROELECTRIC AND HIGH DIELECTRIC CONSTANT CAPACITORS	AGARWAL , VISHNU K.
09058612	6156638	150	04/10/1998	INTEGRATED CIRCUITRY AND METHOD OF RESTRICTING DIFFUSION FROM ONE MATERIAL TO ANOTHER	AGARWAL , VISHNU K.
09074638	6165834	150	05/07/1998	METHOD OF FORMING CAPACITORS, METHOD OF PROCESSING DIELECTRIC LAYERS, METHOD OF FORMING A DRAM CELL	AGARWAL , VISHNU K.
09334842	6365453	150	06/16/1999	METHOD AND STRUCTURE FOR REDUCING CONTACT ASPECT RATIOS	AGARWAL , VISHNU K.
09342212	6306754	150	06/29/1999	METHOD FOR FORMING WIRING WITH EXTREMELY LOW PARASITIC CAPACITANCE	AGARWAL , VISHNU K.
09037235	Not Issued	123	03/10/1998	IN SITU STOICHIOMETRIC CONTROL OF DIRECT LIQUID INJECTION SYSTEM	AGARWAL , VISHNU K.
09059057	Not Issued	071	04/10/1998	CAPACITORS AND METHODS OF FORMING CAPACITORS	AGARWAL , VISHNU K.
09137155	6095085	150	08/20/1998	PHOTO-ASSISTED REMOTE PLASMA APPARATUS AND METHOD	AGARWAL , VISHNU K.

WPF 02/03/03

09225887	Not Issued	041	01/05/1999	CAPACITOR ELECTRODE FOR INTEGRATING HIGH K MATERIALS	AGARWAL , VISHNU K.
09323440	6277687	150	06/01/1999	METHOD OF FORMING A PAIR OF CAPACITORS HAVING A COMMON CAPACITOR ELECTRODE, METHOD OF FORMING DRAM CIRCUITRY, INTEGRATED CIRCUITRY AND DRAM CIRCUITRY	AGARWAL , VISHNU K.
09376232	6198144	150	08/18/1999	PASSIVATION OF SIDEWALLS OF A WORD LINE STACK	AGARWAL , VISHNU K.
09382295	Not Issued	041	08/24/1999	BORON INCORPORATED DIFFUSION BARRIER MATERIAL	AGARWAL , VISHNU K.
09364852	6465828	150	07/30/1999	SEMICONDUCTOR CONTAINER STRUCTURE WITH DIFFUSION BARRIER	AGARWAL , VISHNU K.
09386316	Not Issued	168	08/31/1999	STRUCTURALLY-STABILIZED CAPACITORS AND METHOD OF MAKING OF SAME	AGARWAL , VISHNU K.
09139814	6323046	150	08/25/1998	METHOD AND APPARATUS FOR ENDPOINTING A CHEMICAL-MECHANICAL PLANARIZATION PROCESS	AGARWAL , VISHNU K.
09428125	Not Issued	041	10/26/1999	CAPACITORS AND METHODS OF FORMING CAPACITORS	AGARWAL , VISHNU K.

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="AGARWAL"/>	<input type="text" value="Vishnu K."/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

WPF 02/03/03